

## TT 28: Superconducting Electronics: SQUIDS and other Josephson Circuits and Components

Time: Tuesday 9:30–12:45

Location: CHE/0089

TT 28.1 Tue 9:30 CHE/0089

**Gate-controlled switching in non-centrosymmetric superconducting devices - Large output voltage** — ●JENNIFER KOCH<sup>1</sup>, LEON RUF<sup>1</sup>, ANGELO DI BERNARDO<sup>1,2</sup>, and ELKE SCHEER<sup>1</sup> — <sup>1</sup>Universität Konstanz, Konstanz, Germany — <sup>2</sup>Università degli Studi di Salerno, Fisciano (SA), Italy

Gate-controlled supercurrent (GCS) devices have become of great interest as a superconducting equivalent to complementary metal-oxide-semiconductor (CMOS) logic. The underlying concept is based on the observation that supercurrent can be controlled electrically through the application of a gate voltage [1,2].

We investigate GCS devices made of the non-centrosymmetric superconductor Nb<sub>0.18</sub>Re<sub>0.82</sub>. By combining geometric adjustments with the material's high normal-state resistivity, we achieve a significant increase of the output voltage. The resulting voltage is high enough to drive CMOS transistors, demonstrating the potential of GCS devices to interface directly with conventional semiconductor electronics. This finding represents an important step towards hybrid computing architectures.

[1] De Simoni et al., Nature Nanotech 13, 802 (2018)

[2] Paolucci et al., Nano Letters 18, 4195 (2018)

TT 28.2 Tue 9:45 CHE/0089

**Chemical-mechanical polishing process for the fabrication of cross-type Nb/Al-AIO<sub>x</sub>/Nb Josephson tunnel junctions** — ●ALEXANDER STOLL, LUKAS MÜNCH, DANIEL HENGSTLER, ANDREAS REIFENBERGER, ANDREAS FLEISCHMANN, and CHRISTIAN ENSS — Kirchhoff-Institute for Physics, Heidelberg University, Germany

The core elements at the frontier of superconducting electronic devices, such as qubits or superconducting quantum interference devices (SQUIDS), are the Josephson tunnel junctions (JJs). To enable scaling in production of these devices, good parameter control is required, including a uniform quality and reproducibility. We use a cross-type geometry for our JJs so that our junction area is not limited by alignment inaccuracies and, at the same time, parasitic capacitances and thus parasitic LC resonances can be avoided. A sputtered SiO<sub>2</sub> layer used for the insulation of the structured Nb/Al-AIO<sub>x</sub>/Nb trilayer including its sidewalls usually requires a time-consuming lift-off and leaves behind undesired wings that can compromise the quality of subsequent layers. To mitigate these challenges, we introduced a chemical-mechanical polishing (CMP) step. We present our optimized fabrication process which achieves a well-embedded, smooth, and uniform post-CMP surface and substantially improves the reliability and yield of our JJs on 3 inch wafer-scale. A variety of tests to characterize the JJs based on their IV-characteristics and their Fraunhofer Patterns as well as the electrical properties of the superconducting Nb are discussed.

TT 28.3 Tue 10:00 CHE/0089

**Towards the next generation of dc SQUID sensors** — ●MAURO ESATTORE<sup>1</sup>, OLIVER KIELER<sup>1</sup>, MICHAEL PAULSEN<sup>2</sup>, RAINER KÖRBER<sup>2</sup>, PATRYK KRZYSTECZKO<sup>2</sup>, MARK BIELER<sup>1</sup>, and JÖRN BEYER<sup>2</sup> — <sup>1</sup>Physikalisch-Technische Bundesanstalt, Bundesallee 100, 38116 Braunschweig, Germany — <sup>2</sup>Physikalisch-Technische Bundesanstalt, Abbestraße 2-10, 10587 Berlin, Germany

This is an update on our path towards next-generation SQUID devices, featuring “fine-pitch” input coils and window-type Nb/Al-AIO<sub>x</sub>/Nb Josephson junctions (JJs), both realized with sub-micrometer dimensions. The circuit elements are fabricated using electron beam lithography and are integrated into existing sensor designs which are currently fabricated using UV lithography, which limits the minimal dimensions. For current sensor SQUIDS, it is crucial to maximize the inductive coupling  $k$  between the signal input coil and the SQUID loop to minimize the coupled energy sensitivity  $\varepsilon_c = \varepsilon/k^2$  - with  $\varepsilon$  being the intrinsic energy sensitivity. The SQUID energy sensitivity  $\varepsilon \approx \sqrt{C_{JJ}}$  can also be lowered by reducing the JJ capacitance  $C_{JJ}$ . Our aim is to achieve high sensor compactness as well as reduced coupling losses, without further modifying the sensors design. To that end, we fabricated fine-pitch coils with lateral width down to 0.3  $\mu\text{m}$  - almost an order of magnitude smaller than coils fabricated with UV lithography - with inductance values ranging from 400 nH to 14  $\mu\text{H}$ , depending on the number of coil windings. Details concerning design aspects of

both circuit elements, their fabrication and characterization results are provided.

TT 28.4 Tue 10:15 CHE/0089

**Tapping-mode SQUID-on-tip Microscopy with Proximity Josephson Junctions** — ●MATTHIJS ROG<sup>1</sup>, TYCHO J. BLOM<sup>1</sup>, DAAN B. BOLTJE<sup>1,2</sup>, MILAN P. ALLAN<sup>1,2,3</sup>, and KAVEH LAHABI<sup>1,2</sup> — <sup>1</sup>Institute of Physics, Leiden University, Leiden, The Netherlands — <sup>2</sup>QuantaMap B.V., Leiden, The Netherlands — <sup>3</sup>Faculty of Physics, Ludwig-Maximilians-University Munich, Munich, Germany

Understanding nanoscale dynamics in strongly correlated systems and quantum materials requires investigating the interplay between dissipation, magnetism and electronic transport. The local mapping of transport properties, such as current flow, and their relation to geometry and magnetism still remains a major challenge. Here, we introduce tapping-mode SQUID-on-tip, which combines atomic force microscopy (AFM) with nanoSQUID sensing. This microscope is able to simultaneously image magnetic flux, electrical currents, local heating and sample topography. Our probes minimize nanoSQUID-sample distance, provide in-plane magnetic sensitivity, and operate even on highly corrugated nanostructures and devices. Our fully electronic readout removes the need for optical elements and external radiation. By using proximity-junction nanoSQUIDS with large voltage output, we resolve nanoscale currents down to 100 nA using a simple four-probe electronic readout. In addition to demonstrating the technique, we will show the first applications to strongly correlated electron systems, where our microscope offers immediate new insights into the underlying physics.

[1] M. Rog et al., arXiv:2508.21575

TT 28.5 Tue 10:30 CHE/0089

**Nanoscale SQUID on a wireframe tip cantilever by corner lithography** — ●THIJS ROSKAMP<sup>1</sup>, TIM HORSTINK<sup>2</sup>, MELISSA GOODWIN<sup>1</sup>, ERWIN BERENSCHOT<sup>1</sup>, ROELAND HUIJINK<sup>2</sup>, EDIN SARAJILIC<sup>2</sup>, NIELS TAS<sup>1</sup>, and HANS HILGENKAMP<sup>1</sup> — <sup>1</sup>MESA+ Institute, University of Twente, Enschede, The Netherlands — <sup>2</sup>Bruker Nederland B.V., Bruker Corporation, Leiderdorp, The Netherlands

Superconducting quantum interference devices (SQUIDS) are the most sensitive magnetic flux sensors and are used in scanning SQUID microscopy (SSM) to spatially resolve and map magnetism. Conventional SSM probes make use of planar silicon substrates which limit their spatial resolution to several micrometers due to an increased sample-pickup area spacing.

Employing ideas from other scanning probe techniques like atomic force microscopy, moving the SQUID to the apex of tip on a scanning probe can significantly increase the spatial resolution.

We have used the principles of corner lithography and molding in silicon wafers to create freestanding superconducting wireframe tips on cantilevers on the wafer scale. With a focused ion beam we pattern superconducting weak links at the apex of the fabricated wireframe tips to create SQUIDS with sizes from sub-100 nm to several micrometers. By integrating the wireframe probe on a silicon nitride cantilever with pre-defined contact pads, leads, and resistive strain gauges, we create a SQUID on cantilever probe which will enable simultaneous magnetic and topographic imaging.

TT 28.6 Tue 10:45 CHE/0089

**Low noise amplification using Nb trilayer Dimer Josephson Junction Array Amplifiers** — ●BHOOMIKA R BHAT, ASEN L GEORGIEV, FABIAN KAAP, VICTOR GAYDAMACHENKO, CHRISTOPH KISSLING, JUDITH FELGNER, MARK BIELER, and LUKAS GRÜNHaupt — Physikalisch-Technische Bundesanstalt, Bundesallee 100, 38116 Braunschweig, Germany

Minimizing added noise in amplification is crucial for quantum-based devices requiring microwave readout signals in the femtowatt range. For example, in superconducting qubits, it significantly improves readout fidelity. Extensive efforts over the last decade have been made in the development of parametric amplifiers to address this challenge. We develop a Dimer Josephson Junction Array Amplifier (DJJAA) [1], in which parametric amplification in the degenerate four-wave-mixing regime is facilitated by pairs of resonant modes, referred to as dimers. We design it to feature multiple flux-tunable dimers within the 2 to

8 GHz range, each of which could be utilized for parametric amplification. Our DJJAAs have 900 to 3000 dc-SQUIDS, and we fabricate them using Nb/Al-AlO<sub>x</sub>/Nb trilayer technology. We present the fabrication flow of our devices and provide an overview of the corresponding experimental results. Our devices show gain on the order of 20 dB with bandwidths in the range of 5 to 10 MHz and the typical input saturation powers are on the order of -110 dBm.

[1] P. Winkel et al., Phys. Rev. Applied 13, 024015 (2020).

## 15 min. break

TT 28.7 Tue 11:15 CHE/0089

**Controlling three- and four-wave mixing processes in JTWPAs** — •DANIL BAZULIN<sup>1,2</sup>, JOHANNES SCHIRK<sup>1,2</sup>, NIKLAS BRUCKMOSER<sup>1,2</sup>, LEON KOCH<sup>3</sup>, YONGJIE YUAN<sup>4</sup>, MICHAEL HAIDER<sup>4,5</sup>, STEFAN FILIPP<sup>1,2,6</sup>, and KIRILL G. FEDOROV<sup>1,2,6</sup> —

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Josephson travelling-wave parametric amplifiers (JTWPAs) are essential for scalable quantum computing with superconducting circuits. Their typical bandwidths of several gigahertz, in combination with quantum-limited noise performance, enable high-fidelity multiplexed readout of qubits. In JTWPAs, amplification occurs due to the interaction between the pump and signal modes in a nonlinear medium provided by superconducting nonlinear asymmetric inductive elements (SNAILs). Here, we experimentally investigate a robust JTWPA design based on SNAILs that is capable of operating in both three- and four-wave-mixing regimes. Our results show that we can fully suppress the three-wave mixing process while observing over 10 dB gain from the four-wave pumping over the bandwidth of 4 GHz.

TT 28.8 Tue 11:30 CHE/0089

**Demonstration of natively phase-matched parametric amplification in a left-handed transmission line** — •CHRISTOPH KISSLING<sup>1</sup>, VICTOR GAYDAMACHENKO<sup>1</sup>, FABIAN KAAP<sup>1</sup>, MELANIE ZIEGLER<sup>2</sup>, HANNES TOEPFER<sup>2</sup>, and LUKAS GRÜNHaupt<sup>1</sup> — <sup>1</sup>Physikalisch-Technische Bundesanstalt, Bundesallee 100, 38116 Braunschweig — <sup>2</sup>Technische Universität Ilmenau, Ehrenbergstraße 29, 98693 Ilmenau

Wideband amplification of weak microwave signals is a key ingredient for cutting-edge experiments ranging from quantum information processing to the search for dark matter. A class of devices that achieves this is the traveling-wave parametric amplifier (TWPA). So far, all TWPA implementations are based on right-handed transmission lines, which exhibit a positive refractive index. Recently, an alternative approach has been proposed that utilizes left-handed transmission lines, which have a negative refractive index [1]. This property allows for self-phase-matched parametric amplification in the four-wave-mixing regime, eliminating the need for any engineering of nonlinearity or dispersion for phase matching. Here, we present our implementation of this concept, which employs a left-handed transmission line with nonlinear inductors made from a granular aluminum thin film. First results show a signal power gain exceeding 10 dB, dynamically tunable across multiple GHz. With only one lithography step, and an order-of-magnitude reduction in circuit length, the concept is promising to significantly reduce the complexity of designing and fabricating a TWPA.

[1] C. Kow et al., Phys. Rev. Applied 24, 024026 (2025)

TT 28.9 Tue 11:45 CHE/0089

**Towards 1 V Josephson Arbitrary Waveform Synthesizer** — •OMAR M. ALADDIN<sup>1</sup>, OLIVER KIELER<sup>1</sup>, ABDULRAHMAN WIDAA<sup>1</sup>, HANNES PREISLER<sup>1</sup>, ERASMUS WOLF<sup>2</sup>, MARCO SCHUBERT<sup>2</sup>, JUDITH FELGNER<sup>1</sup>, ROLF-WERNER GERDAU<sup>1</sup>, JOHANNES KOHLMANN<sup>1</sup>, and MARK BIELER<sup>1</sup> — <sup>1</sup>Physikalisch-Technische Bundesanstalt, Bundesallee 100, 38116 Braunschweig, Germany — <sup>2</sup>Supracon AG, An der Lehmgrube 11, 07751 Jena, Germany

The Josephson Arbitrary Waveform Synthesizer (JAWS) provides quantum-accurate AC voltage waveforms with high spectral purity, low noise, and inherent long-term stability. Based on pulse-driven

Josephson junction (JJ) arrays, JAWS is established as the primary AC voltage standard. Recent JAWS developments at PTB include increasing the output voltage per chip, targeting 1 V RMS to advance its use in high-accuracy metrological applications [1]. Current efforts focus on enhancing the JAWS fabrication process to enable larger array sizes using 5-stacked JJ arrays. The stacked junction technology is combined with improved on-chip Wilkinson power dividers, allowing the incorporation of more than two JJ arrays on one chip [2]. The power dividers have been successfully demonstrated with 3-stacked JJ arrays achieving 600 mV RMS per JAWS chip with a total of 36,000 junctions. In this contribution, we present the recent results of 5-stacked JJ arrays and Wilkinson power dividers with up to 60,000 JJs per JAWS chip.

[1] DOI: 10.1016/B978-0-323-90800-9.00001-9

[2] DOI: 10.1109/TASC.2021.3055161

TT 28.10 Tue 12:00 CHE/0089

**Study on the feedthrough error in quantum-based superconducting RF waveform generators** — •MICHAEL HAAS, ABDULRAHMAN WIDAA, OLIVER KIELER, MARCO KRAUS, RALF BEHR, JOHANNES KOHLMANN, and MARK BIELER — Physikalisch-Technische Bundesanstalt, Bundesallee 100, 38116 Braunschweig, Germany

Quantum-based signal generators are of great interest for a variety of applications such as electrical metrology or quantum computing. The Josephson Arbitrary Waveform Synthesizer (JAWS) consisting of an array of Josephson junctions is one of such devices. It allows the generation of arbitrary, low-noise waveforms with high spectral purity by utilizing the Josephson effect. This approach is well established for signal frequencies in the kHz to low MHz range. However, modern applications typically demand increasing signal frequencies, reaching the microwave range. To satisfy the need for higher JAWS output frequencies, modified circuit designs and bias schemes are necessary. This is mainly because of the so-called feedthrough error, which denotes an input signal being fed through to the output. We will present recent developments of GHz-JAWS at PTB, comprising detailed investigations on the feedthrough error and methods for its reduction.

TT 28.11 Tue 12:15 CHE/0089

**Nonequilibrium plasmon fluid in a Josephson junction chain** — ANTON V. BUBIS<sup>1</sup>, •LUCIA VIGLIOTTI<sup>1</sup>, MAKSYM SERBYN<sup>1</sup>, and ANDREW P. HIGGINBOTHAM<sup>2</sup> — <sup>1</sup>Institute of Science and Technology Austria, Am Campus 1, Klosterneuburg, 3400, Austria — <sup>2</sup>James Franck Institute and Department of Physics, University of Chicago, 929 E 57th St, Chicago, Illinois 60637, USA

With the recent push towards the development of quantum technologies, multimode quantum systems, such as superconducting resonators, have drawn considerable attention. These systems can be generally described as weakly nonlinear bosonic modes coupled to a thermal bath and subject to coherent driving. As the number of modes grows and extrinsic decoherence is reduced, understanding the mode-to-mode interaction becomes increasingly relevant, especially far from equilibrium. We consider the interacting plasmonic modes emerging in a long chain of Josephson junctions (JJs), probed via multitone microwave spectroscopy. We investigate the nonequilibrium kinetics of the resulting one-dimensional quantum fluid both theoretically and experimentally, focusing on four-wave-mixing processes. Under two coherent drives, we observe cascaded coupling between plasmonic modes, reproduced using input-output theory applied to nonlinear mode multiplets. Under incoherent broadband drive, we explore the kinetics of weakly populated modes and numerically implement a kinetic equation that predicts the non-equilibrium steady state and captures the excess linewidth of non-driven modes. Our work establishes the key role of four-wave-mixing nonlinearities in the non-equilibrium response of JJ chains.

TT 28.12 Tue 12:30 CHE/0089

**Superconducting non-volatile memory device based on charge trapping in Al<sub>2</sub>O<sub>3</sub>.** — •LEON RUF<sup>1</sup>, JENNIFER KOCH<sup>1</sup>, ANGELO DI BERNARDO<sup>1,2</sup>, and ELKE SCHEER<sup>1</sup> — <sup>1</sup>Department of Physics, University of Konstanz, 78464 Konstanz, Germany — <sup>2</sup>Department of Physics, University of Salerno, 84084 Salerno, Italy

Gate-controlled supercurrent (GCS) is a debated research topic. Experiments on three-terminal devices have shown that applying a gate voltage can modulate the supercurrent [1]. The authors interpret their findings as a direct electric-field effect, suggesting potential for CMOS-compatible superconducting transistors.

In contrast, other studies suggest that the observed modulation stems from a small leakage current flowing within the substrate [2,3],

which can be as small as a few fA causing nonequilibrium phonons and/or electrons suppressing the supercurrent. A leakage current flowing through the substrate is typically undesirable for applications.

Here, instead, we show that the charge-trapping properties of the  $\text{Al}_2\text{O}_3$  substrate can be harnessed to realize a non-volatile superconducting memory device based on the GCS effect [4]. We outline the

device concept and operating principle and provide an outlook on future avenues for device optimization.

[1] De Simoni et al., Nat. Nanotechnol. 13, 802 (2018).

[2] Ritter et al., Nat. Electron. 5, 71 (2022).

[3] Basset et al., Phys. Rev. Research 3, 043169 (2021).

[4] Ruf et al., arXiv:2503.17241 (2025).